



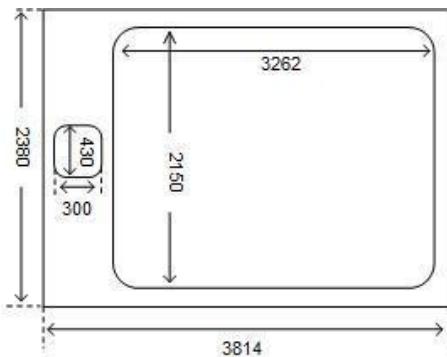
NDM60S7W

Rev.1.0

Fast Recovery N-channel MOSFET (Ultra Low Tr_r)

2021/9/27

Mechanical Data



Item	Information	
Chip Size	3874*2440	um ²
Gate Pad Size	300*430	
Source Pad Size	3262*2150	um
Scribe Line Width	60	
Wafer Thickness	254	mm
Wafer diameter	150	
Source Metallization	Al, Si, Cu	
Drain Metallization	Ti, Ni, Ag	
Reject Ink Dot Size	ø 10mil	
Recommended Storage Environment	Store in original container, in dry nitrogen, (6 months at an ambient temperature of 23°C+/-3°C)	

Electrical characteristics in C/P test @T_j=25 °C

Parameter	Description	Min.	Typ.	Max.	Unit	Test Condition
V _{(BR)DSS}	Drain-Source Breakdown Voltage	600	-	-	V	V _{GS} =0V, ID=250uA
R _{Ds(ON)}	Static Drain-Source On-Resistance	-	1.36	2	Ω	V _{GS} =10V, ID=3.5A
V _{GS(th)}	Gate Threshold Voltage	1.5	-	4	V	V _{GS} =V _{GS} , ID=250uA
I _{DSS}	Drain-to-Source Leakage Current	-	-	0.1	uA	V _{Ds} =600V, V _{GS} =0V
I _{GSS}	Gate-Body Leakage Current	-	-	0.1	uA	V _{Ds} =0V, V _{GS} =±30V

Source to drain diode ratings characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
I _s	Continuous source current	Integral reverse p-n Junction current	-	-	5.5	A
I _{SM}	Pulsed source current		-	-	22	A
V _{SD}	Diode forward voltage drop	I _s =3.5A, V _{GS} =0V	-	0.9	1.2	V
Tr _r	Reverse recovery time	I _s =1A, V _{DD} =30V V _{GS} =0V, dI _F /dt=50A/us	-	70		ns